FIGURE 1

PRIOR ART

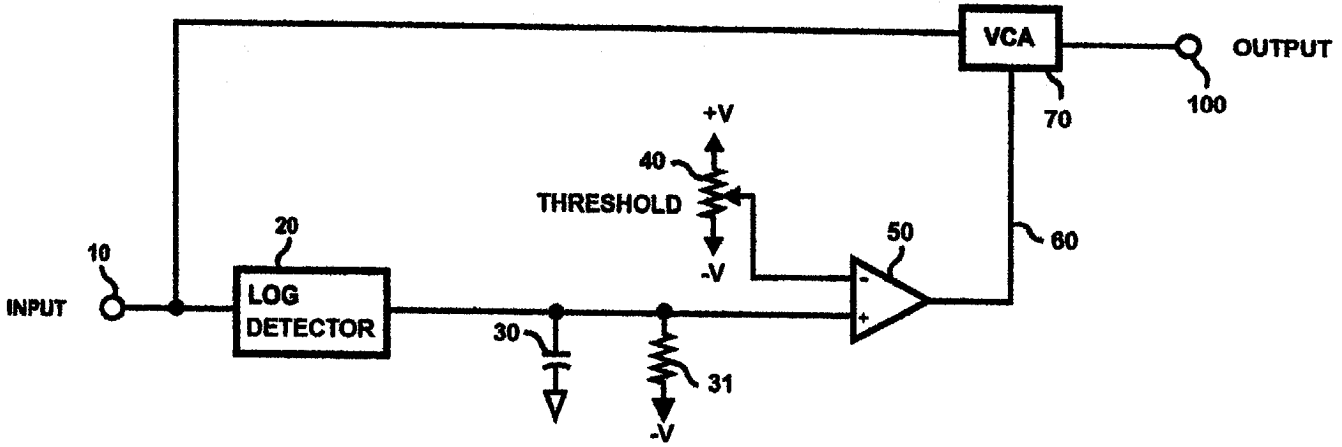


FIGURE 2

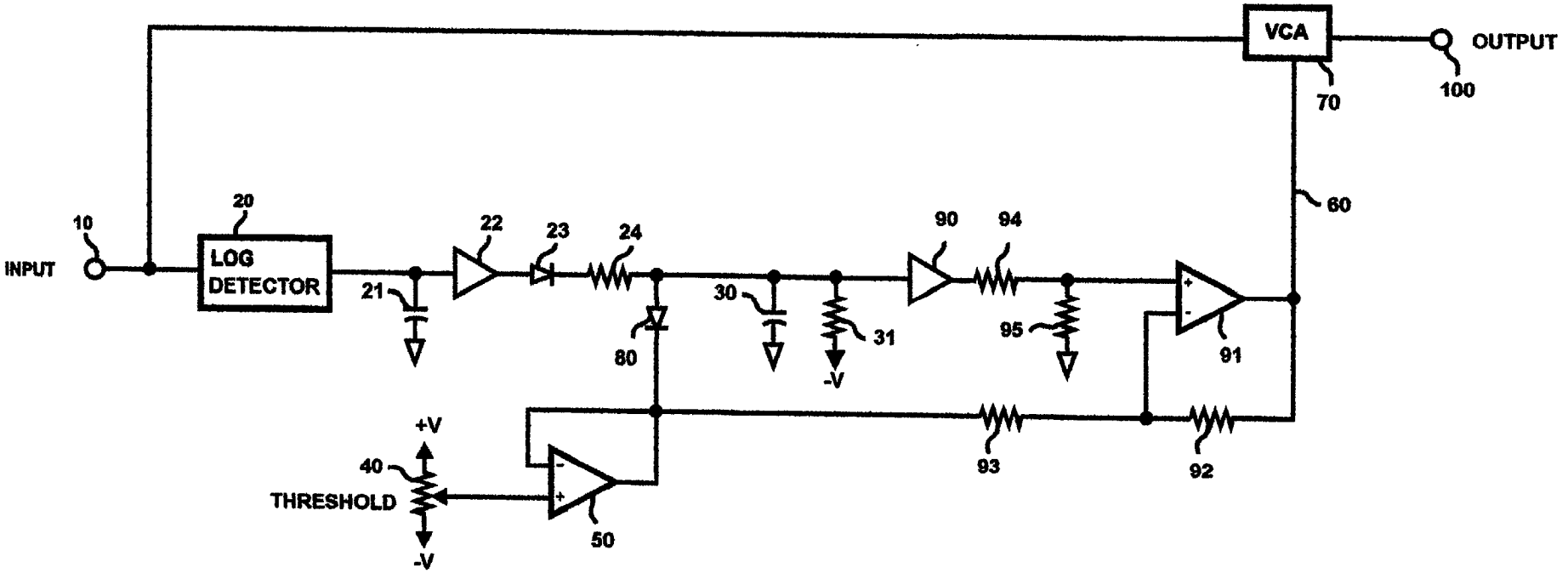


FIGURE 4

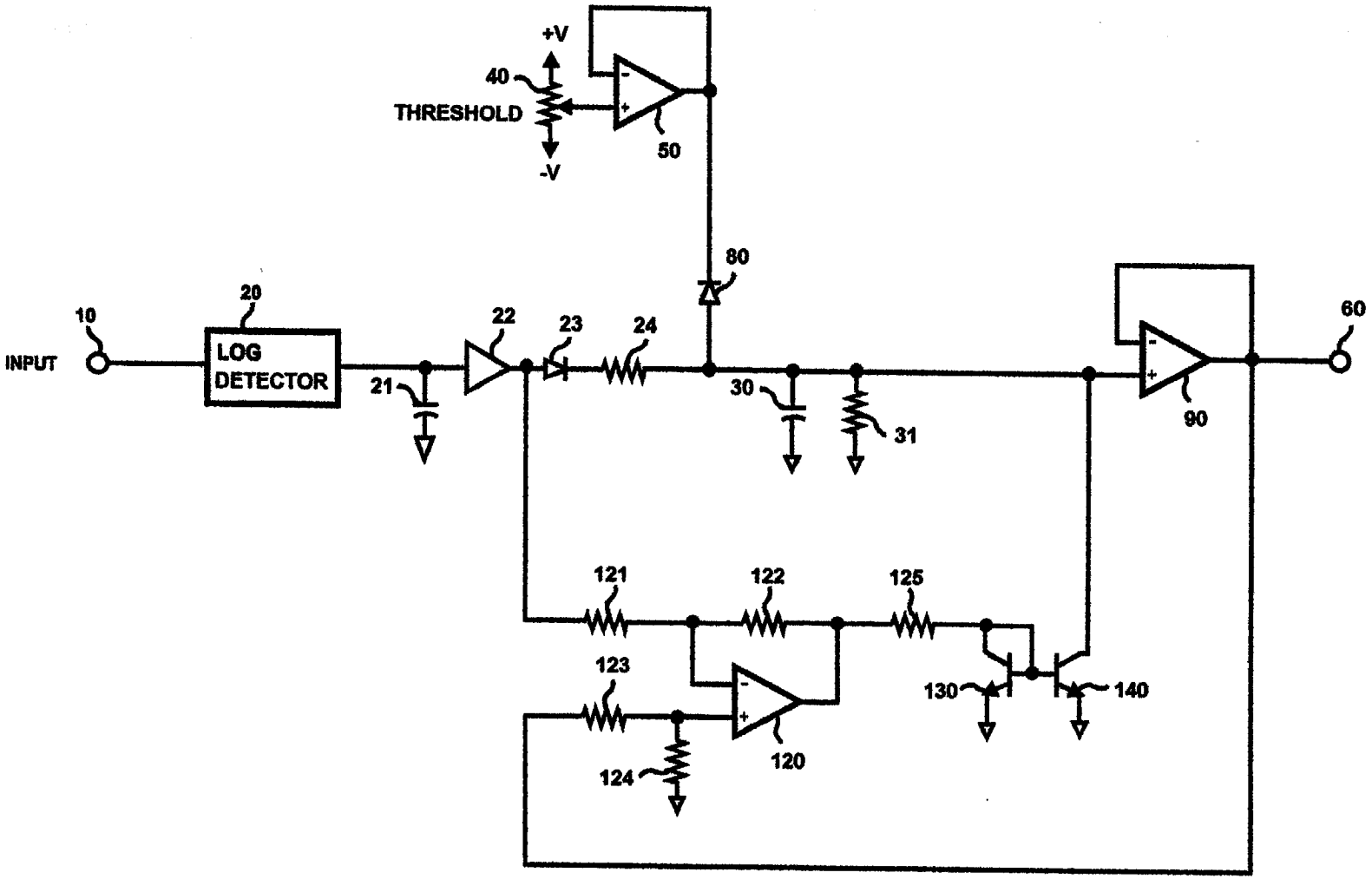
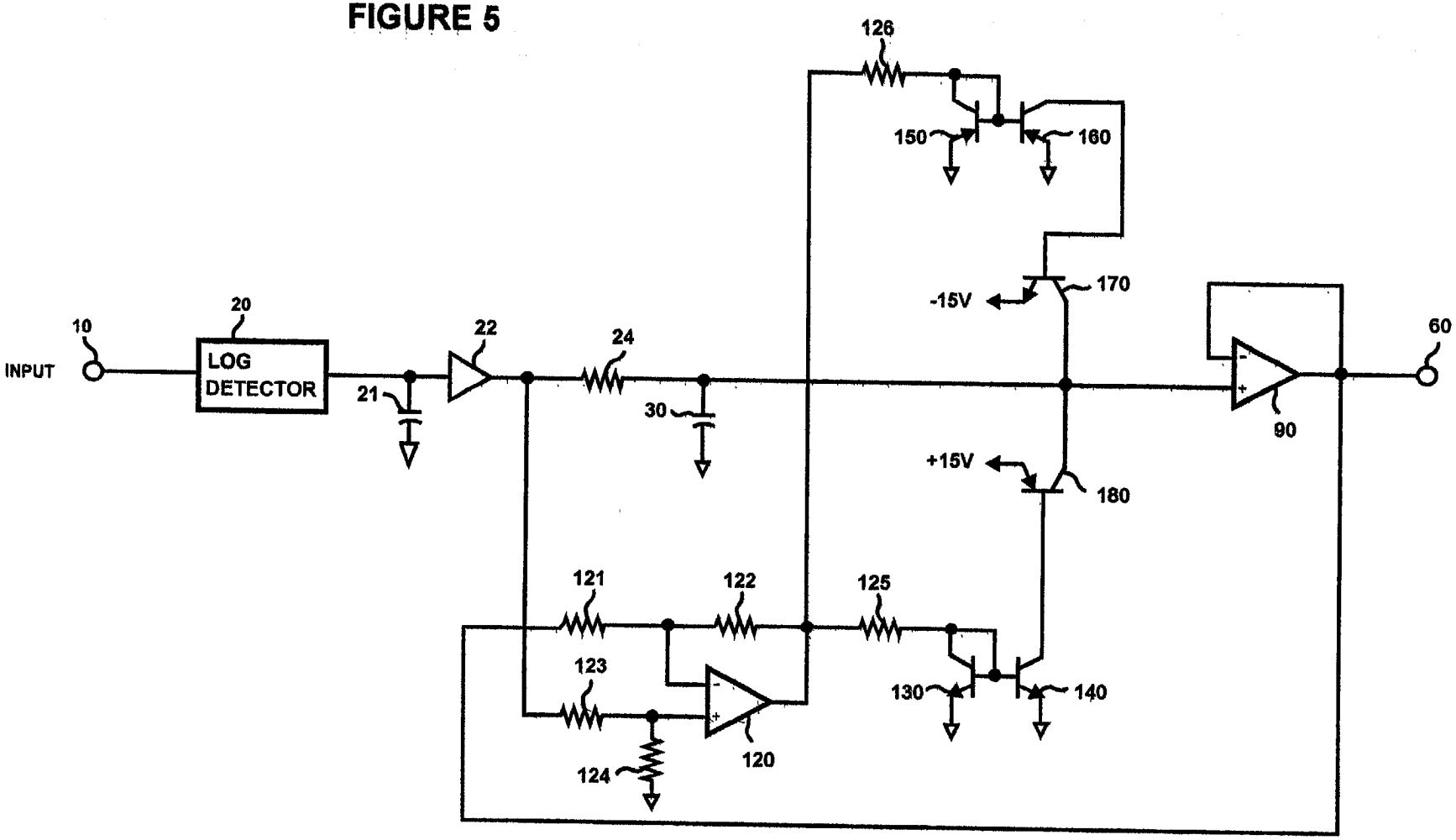

+V
40
THRESHOLD
-V
50
80
10
INPUT
20
LOG
DETECTOR
21
22
23
24
30
31
90
60
121
122
125
123
124
120
130
140

FIGURE 5


126
150
160
-15V
170
INPUT
10
20
LOG
DETECTOR
21
22
24
30
60
90
+15V
180
121
122
125
123
124
120
130
140

TOE2hO" Sh2Oh860

FIGURE 3

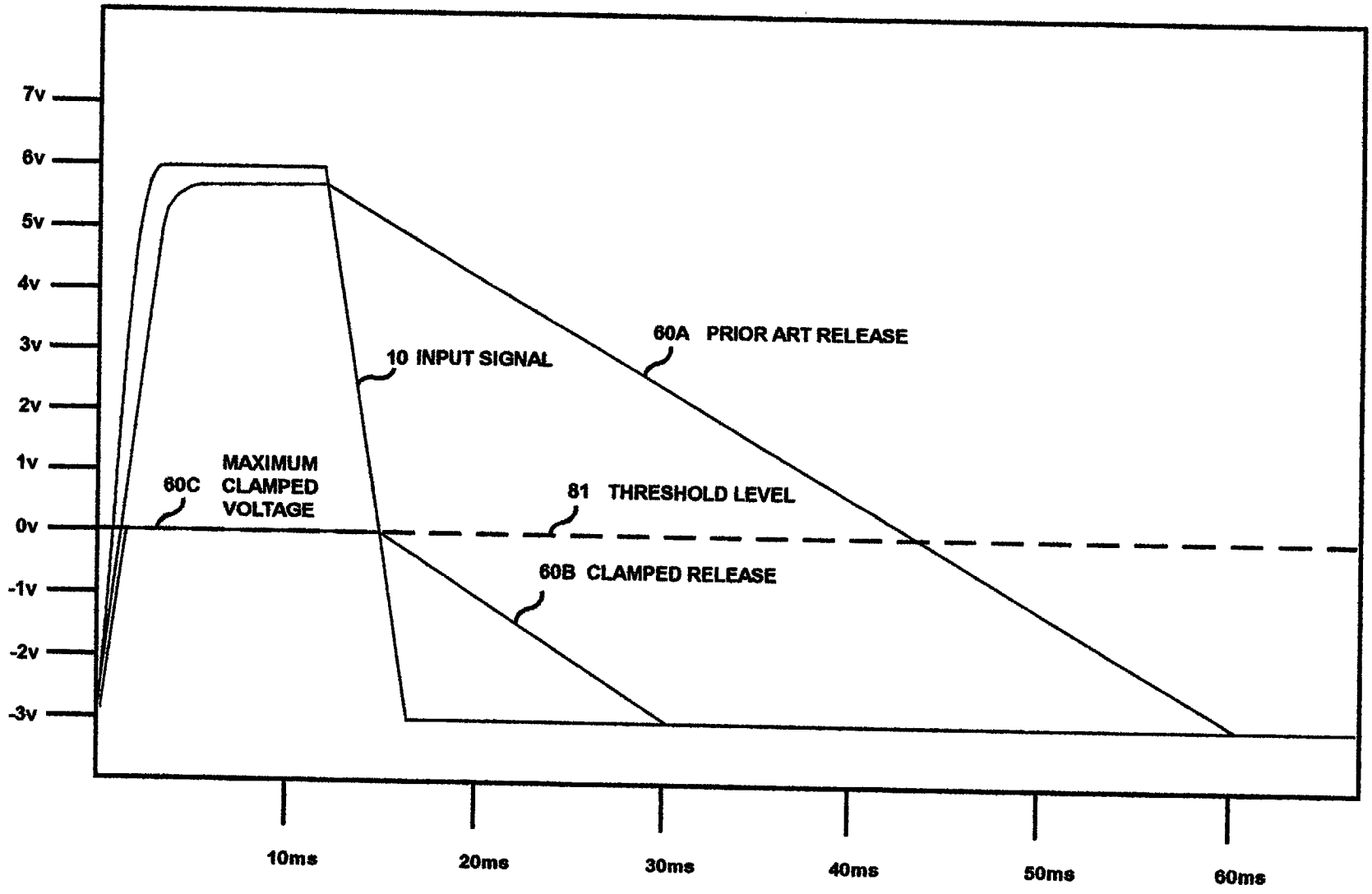
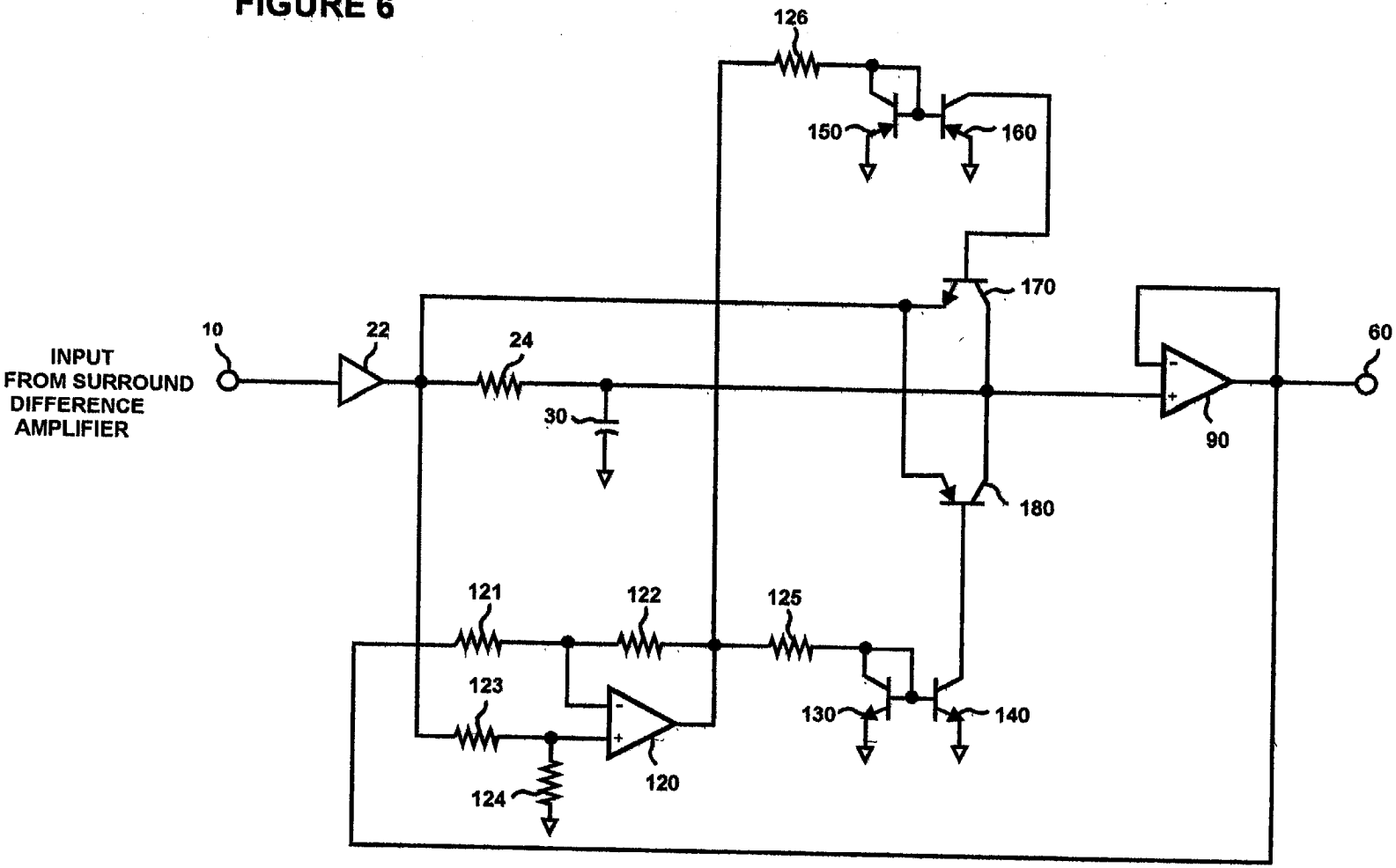


FIGURE 6


INPUT
FROM SURROUND
DIFFERENCE
AMPLIFIER
10
22
24
30
126
150
160
170
180
90
60
121
122
125
123
124
120
130
140